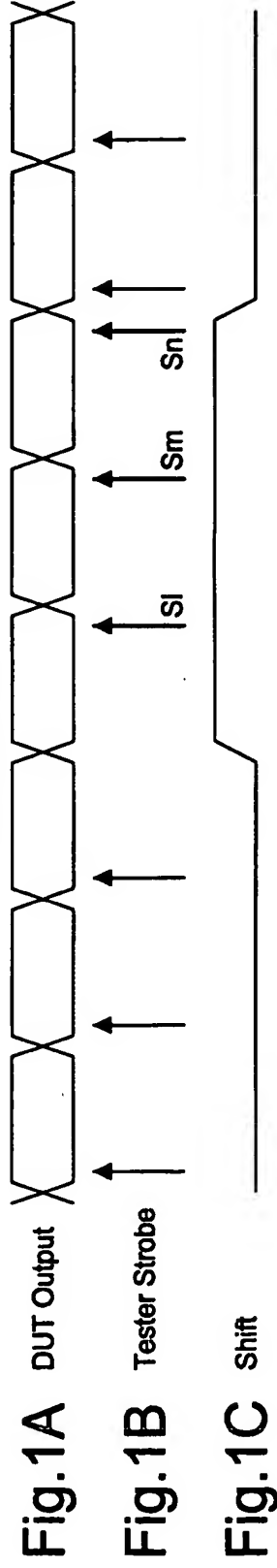


Approved 3/17/05 *MS*

Replacement Sheet



Prior Art





Prior Art

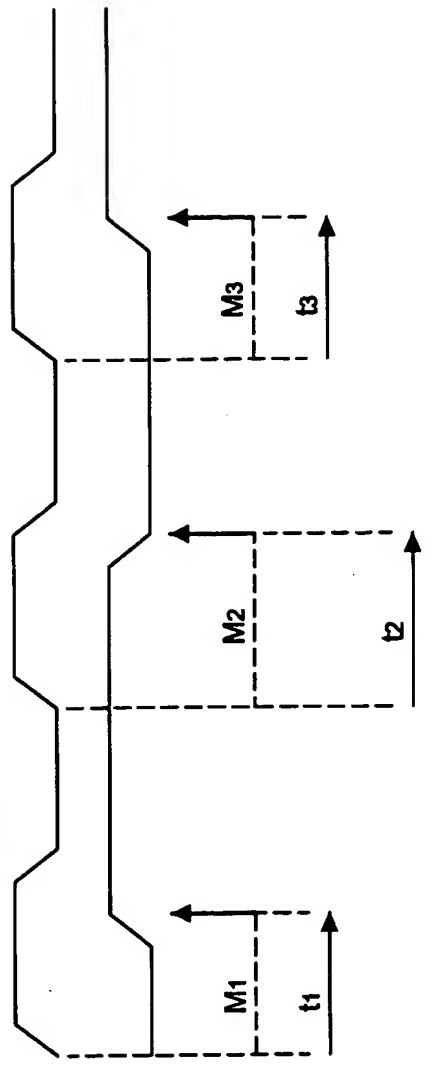


Fig.2A DUT Clock Input

Fig.2B DUT Output

Fig.2C Tester Strobe Point

Fig.2D DUT Delay : L → H

Fig.2E DUT Delay : H → L